

Bit 0	Require Mask R_0	1010	Veto Mask V_0	0100	Prescale Probability P_0	0	Selector S_0	0	Enable E_0	1
	Trigger word T	1010	Trigger word T	1010	Random number x_0	49732	Channel C	0		
	Result $(T \& R_0) == R_0$	1	Result $(\sim T \& V_0) == V_0$	1	Result $x_0 \geq P_0$	1	Result $S_0 == C$	1	Result E_0	1
	AND of all results is 1, so Bit 0 is 1									
Bit 1	Require Mask R_1	1011	Veto Mask V_1	0000	Prescale Probability P_1	0	Selector S_1	0	Enable E_1	1
	Trigger word T	1010	Trigger word T	1010	Random number x_1	53441	Channel C	0		
	Result $(T \& R_1) == R_1$	0	Result $(\sim T \& V_1) == V_1$	1	Result $x_1 \geq P_1$	1	Result $S_1 == C$	1	Result E_1	1
	AND of all results is 0, so Bit 1 is 0									
Bit 2	Require Mask R_2	1010	Veto Mask V_2	0000	Prescale Probability P_2	32768	Selector S_2	0	Enable E_2	1
	Trigger word T	1010	Trigger word T	1010	Random number x_2	19488	Channel C	0		
	Result $(T \& R_2) == R_2$	1	Result $(\sim T \& V_2) == V_2$	1	Result $x_2 \geq P_2$	0	Result $S_2 == C$	1	Result E_2	1
	AND of all results is 0, so Bit 2 is 0									
Bit 3	Require Mask R_3	1000	Veto Mask V_3	0010	Prescale Probability P_3	128	Selector S_3	0	Enable E_3	1
	Trigger word T	1010	Trigger word T	1010	Random number x_3	17308	Channel C	0		
	Result $(T \& R_3) == R_3$	1	Result $(\sim T \& V_3) == V_3$	0	Result $x_3 \geq P_3$	1	Result $S_3 == C$	1	Result E_3	1
	AND of all results is 0, so Bit 3 is 0									
Bit 4	Require Mask R_4	1010	Veto Mask V_4	0000	Prescale Probability P_4	4192	Selector S_4	0	Enable E_4	0
	Trigger word T	1010	Trigger word T	1010	Random number x_4	23120	Channel C	0		
	Result $(T \& R_4) == R_4$	1	Result $(\sim T \& V_4) == V_4$	1	Result $x_4 \geq P_4$	1	Result $S_4 == C$	1	Result E_4	0
	AND of all results is 0, so Bit 4 is 0									
Bit 5	Require Mask R_5	1010	Veto Mask V_5	0000	Prescale Probability P_5	2048	Selector S_5	2	Enable E_5	1
	Trigger word T	1010	Trigger word T	1010	Random number x_5	65386	Channel C	0		
	Result $(T \& R_5) == R_5$	1	Result $(\sim T \& V_5) == V_5$	1	Result $x_5 \geq P_5$	1	Result $S_5 == C$	0	Result E_5	1
	AND of all results is 0, so Bit 5 is 0									
Bit 6	Require Mask R_6	0101	Veto Mask V_6	1000	Prescale Probability P_6	2048	Selector S_6	2	Enable E_6	1
	Trigger word T	1010	Trigger word T	1010	Random number x_6	11472	Channel C	0		
	Result $(T \& R_6) == R_6$	0	Result $(\sim T \& V_6) == V_6$	0	Result $x_6 \geq P_6$	1	Result $S_6 == C$	0	Result E_6	1
	AND of all results is 0, so Bit 6 is 0									
Bit 7	Require Mask R_7	0000	Veto Mask V_7	0000	Prescale Probability P_7	0	Selector S_7	2	Enable E_7	0
	Trigger word T	1010	Trigger word T	1010	Random number x_7	54470	Channel C	0		
	Result $(T \& R_7) == R_7$	1	Result $(\sim T \& V_7) == V_7$	1	Result $x_7 \geq P_7$	1	Result $S_7 == C$	0	Result E_7	0
	AND of all results is 0, so Bit 7 is 0									